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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Albert S. Weiner

PATENT APPLICATION

Serial No.: 10/810,035

Group Art Unit: 2818

Filed: March 26, 2004

Examiner: T.T. Nguyen

For: NON-VOLATILE TRANSISTOR MEMORY ARRAY  
INCORPORATING READ-ONLY ELEMENTS  
WITH SINGLE MASK SET

Response to Restriction Requirement

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Sir:

In response to the Restriction Requirement in the Office action mailed 07/27/2005 from the Patent and Trademark Office regarding the above-identified patent application, Applicant elects to prosecute the invention of Group I, claims 1-10. Applicant withdraws the non-elected claims 11-19.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Signed: Merle P. Garcia  
Typed Name: Merle P. Garcia

Date: August 2, 2005